PCAN-miniPCIe FD
CAN FD Interface for PCI Express Mini (PCIE)

User Manual

Document version 1.2.0 (2019-09-10)
Relevant products

<table>
<thead>
<tr>
<th>Product name</th>
<th>Model</th>
<th>Part number</th>
</tr>
</thead>
<tbody>
<tr>
<td>PCAN-miniPCiE FD Single Channel</td>
<td>One CAN channel, galvanic isolation for CAN connection</td>
<td>IPEH-004045</td>
</tr>
<tr>
<td>PCAN-miniPCiE FD Dual Channel</td>
<td>Two CAN channels, galvanic isolation for CAN connections</td>
<td>IPEH-004046</td>
</tr>
<tr>
<td>PCAN-miniPCiE FD Four Channel</td>
<td>Four CAN channels, galvanic isolation for CAN connections</td>
<td>IEPH-004047</td>
</tr>
</tbody>
</table>

The cover picture shows the product PCAN-miniPCiE FD Dual and Four Channel. The Single Channel model has an identical form factor like the Dual Channel version but varies in equipment.
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1 Introduction

The PCAN-miniPCIe FD is a CAN interface for the PCI Express Mini slot. With its space-saving format, the plug-in card is a suitable solution for connecting embedded systems to up to four CAN FD and CAN networks. There is galvanic isolation of up to 300 Volts between the computer and CAN sides. The card is available as a single, dual, or four-channel version.

The new CAN FD standard (CAN with Flexible Data rate) is primarily characterized by higher bandwidth for data transfer. The maximum of 64 data bytes per CAN FD frame (instead of 8 so far) can be transmitted with bit rates up to 12 Mbit/s. CAN FD is downward-compatible to the CAN 2.0 A/B standard, thus CAN FD nodes can be used in existing CAN networks. However, in this case the CAN FD extensions are not applicable.

The monitor software PCAN-View and the programming interface PCAN-Basic for the development of applications with CAN connection are included in the scope of supply and support the new standard CAN FD.

**Tip:** At the end of this manual (Appendix C) you can find a Quick Reference with brief information about the installation and operation of the PCAN-miniPCIe card.

1.1 Properties at a Glance

- CAN interface for the PCI Express Mini slot
- 1, 2, or 4 High-speed CAN channels (ISO 11898-2)
- Form factor PCI Express Mini (only for single and dual channel versions due to the height of the four-channel version)
Complies with CAN specifications 2.0 A/B and FD
CAN FD support for ISO and Non-ISO standards switchable
CAN FD bit rates for the data field (64 bytes max.) from 25 kbit/s up to 12 Mbit/s
CAN bit rates from 25 kbit/s up to 1 Mbit/s
CAN bus connection via connection cable and D-Sub, 9-pin (in accordance with CiA® 303 -1)
FPGA implementation of the CAN FD controller
NXP TJA1044GT CAN transceiver
Galvanic isolation on the CAN connection up to 300 V, separate for each CAN channel
CAN termination can be activated through a solder jumper, separately for each CAN channel
Measurement of bus load including error frames and overload frames on the physical bus
Induced error generation for incoming and outgoing CAN messages
Extended operating temperature range from -40 to 85 °C (-40 to 185 °F)

Note: This manual describes the use of the PCAN-miniPCIe card with Windows. You can find device drivers for Linux and the corresponding application information on the provided DVD in the Develop directory branch and on our website under www.peak-system.com/linux.
1.2 System Requirements

- A vacant PCI Express Mini slot in the computer (specification 1.x)
- Operating system Windows 10, 8.1, 7 (32/64-bit) or Linux (32/64-bit)

1.3 Scope of Supply

- PCAN-miniPCIe FD card
- Connection cable including D-Sub plug for each channel
- Device drivers for Windows 10, 8.1, 7 and Linux (32/64-bit)
- CAN monitor PCAN-View for Windows
- Programming interface PCAN-Basic for developing applications with CAN connection
- Programming interfaces for standardized protocols from the automotive sector
- Manual in PDF format
2 Installing the Software and the Card

This chapter covers the software setup for the PCAN-miniPCIe FD card in Windows and the installation of the card in the computer.

Install the driver before you install the card.

Do the following to install the driver:

1. Start Intro.exe from the supplied DVD.
   The navigation program starts.
2. In the main menu, select Drivers and click on Install now.
3. Confirm the message of the User Account Control related to "Installer database of PEAK Drivers".
   The driver setup starts.
4. Follow the program instructions.

Do the following to install the card:

Attention! Electrostatic discharge (ESD) can damage or destroy components on the card. Take precautions to avoid ESD.

1. Shut down the computer.
2. Disconnect the computer’s power supply.
3. Open the computer case.
4. Insert the PCAN-miniPCIe FD card into an empty PCI Express Mini slot.
5. For each CAN channel, mount a D-Sub connector with connection circuit board into a respective hole of the computer casing.
For each CAN channel interconnect a D-Sub connector and the corresponding port on the PCAN-miniPCIe FD card.

Close the computer case.

Reconnect the computer power supply.

Turn on the computer and start Windows. Windows detects the new hardware and completes the driver installation.

Do the following to check the operational readiness:

1. Open the Windows Start menu.
2. Type `peakcpl` and press `Enter`.

The information window for PEAK hardware appears. The plug-in card must be displayed in the table on the CAN Hardware tab.
3 Connecting the CAN Bus

3.1 Connection over D-Sub Connector

A High-speed CAN bus (ISO 11898-2) is connected to the 9-pin D-Sub connector. The pin assignment for CAN corresponds to the specification CiA® 303-1.

Figure 1: Positions of the CAN ports on the PCAN-miniPCIe FD card; left four-channel and right two-channel card; for the single channel card only CAN 1 is present.

Figure 2: Pin assignment of High-speed CAN; male connector on the card (IPEH-004045/46/47)
To connect a CAN bus to the PCAN-miniPCIe FD card, use the supplied special connection cables. After you’ve plugged in the cable on the card, you can connect a CAN bus to the D-sub socket.

The pin assignment between the D-Sub port and the 4-pin connector\(^1\) on the PCAN-miniPCIe FD card is as follows:

![Figure 4: Front view of a CAN connector (SUR) on the PCAN-miniPCIe FD card (CAN 1 to 4)](image)

\(^1\) Connector type SUR from JST (www.jst-mfg.com), name of the matching plug: SM05B-SUR-TF
### 3.2 Activating the Daisy Chain

The daisy chain can be activated via solder jumpers on the board to connect a CAN FD optimized connection to an existing CAN bus. This makes interference-free operation possible at higher CAN FD bit rates, because stubs and Y distributions are largely avoided.

When the daisy chain is activated, the pin assignment changes as follows:

<table>
<thead>
<tr>
<th>Pin SUR</th>
<th>Function</th>
<th>Pin D-Sub</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>Not connected</td>
<td>-</td>
</tr>
<tr>
<td>2</td>
<td>Not connected</td>
<td>-</td>
</tr>
<tr>
<td>3</td>
<td>GND</td>
<td>3, 6</td>
</tr>
<tr>
<td>4</td>
<td>CAN_H</td>
<td>7</td>
</tr>
<tr>
<td>5</td>
<td>CAN_L</td>
<td>2</td>
</tr>
</tbody>
</table>

![Figure 5: Pin assignment of High-speed CAN and daisy chain; male connector on the card (IPEH-004045/46/47)](image)

![Figure 6: Front view of a CAN connector (SUR) on the PCAN-miniPCIe FD card (CAN 1 to 4)](image)
Do the following to activate the daisy chain:

**Risk of short circuit!** Solder with great care to avoid unwanted short circuits on the card.

**Attention!** Electrostatic discharge (ESD) can damage or destroy components on the card. Take precautions to avoid ESD.

Set the solder bridges corresponding to the desired settings.

Figure 7 on page 17 shows the positions of the solder fields on the bottom of the card (IPEH-004045/46/47). The table below contains the possible settings.

Figure 8 on page 15 shows the positions of the solder fields on the bottom of the extension card of the four-channel card (IPEH-004047). The table below contains the possible settings.

<table>
<thead>
<tr>
<th>Pin SUR</th>
<th>Function</th>
<th>Pin D-Sub</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>CAN_H daisy chain</td>
<td>8</td>
</tr>
<tr>
<td>2</td>
<td>CAN_L daisy chain</td>
<td>1</td>
</tr>
<tr>
<td>3</td>
<td>GND</td>
<td>3, 6</td>
</tr>
<tr>
<td>4</td>
<td>CAN_H</td>
<td>7</td>
</tr>
<tr>
<td>5</td>
<td>CAN_L</td>
<td>2</td>
</tr>
</tbody>
</table>
Figure 7: Position of the solder fields on the bottom side of the card (IPEH-004045/46/47) for the activation of the daisy chain

<table>
<thead>
<tr>
<th>D-Sub connector</th>
<th>Solder fields</th>
<th>Daisy chain</th>
</tr>
</thead>
<tbody>
<tr>
<td>CAN1</td>
<td>JP1 and JP2</td>
<td></td>
</tr>
<tr>
<td>CAN2</td>
<td>JP3 and JP4</td>
<td></td>
</tr>
</tbody>
</table>
Figure 8: Position of the solder fields on the top side of the expansion card of the four-channel card (IPEH-004047) for the activation of the daisy chain

<table>
<thead>
<tr>
<th>D-Sub connector</th>
<th>Solder fields</th>
<th>Daisy chain</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td></td>
<td>Without (standard)</td>
</tr>
<tr>
<td>CAN3</td>
<td>JP1 and JP2</td>
<td></td>
</tr>
<tr>
<td>CAN4</td>
<td>JP3 and JP4</td>
<td></td>
</tr>
</tbody>
</table>
3.3 Activating the Internal Termination

The internal termination can be activated by solder jumpers on the board to terminate one end of the CAN bus with 120 Ohms. At delivery the termination is not activated. A High-speed CAN bus (ISO 11898-2) must be terminated on both ends with 120 Ohms. Otherwise disturbances may arise.

Do the following to activate the internal termination:

- **Risk of short circuit!** Solder with great care to avoid unwanted short circuits on the card.

- **Attention!** Electrostatic discharge (ESD) can damage or destroy components on the card. Take precautions to avoid ESD.

Set the solder bridges corresponding to the desired settings.

Figure 9 on page 17 shows the positions of the solder fields on the bottom of the card (IPEH-004045/46/47). The table below contains the possible settings.

Figure 10 on page 18 shows the positions of the solder fields on the bottom of the extension card of the four-channel card (IPEH-004047). The table below contains the possible settings.
Figure 9: Position of the solder fields on the bottom side of the card (IPEH-004045/46/47) for the activation of the internal termination

<table>
<thead>
<tr>
<th>D-Sub connector</th>
<th>Solder fields</th>
<th>Internal termination</th>
</tr>
</thead>
<tbody>
<tr>
<td>CAN1</td>
<td>JP101 and JP102</td>
<td>Without (standard)</td>
</tr>
<tr>
<td>CAN2</td>
<td>JP201 and JP202</td>
<td>Active</td>
</tr>
</tbody>
</table>
Figure 10: Position of the solder fields on the top side of the expansion card of the four-channel card (IPEH-004047) for the activation of the internal termination

<table>
<thead>
<tr>
<th>D-Sub connector</th>
<th>Solder fields</th>
<th>Internal termination</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td></td>
<td>Without (standard)</td>
</tr>
<tr>
<td>CAN3</td>
<td>JP101 and JP102</td>
<td></td>
</tr>
<tr>
<td>CAN4</td>
<td>JP201 and JP202</td>
<td></td>
</tr>
</tbody>
</table>
3.4 Cabling

3.4.1 Termination

The High-speed CAN bus (ISO 11898-2) must be terminated with 120 ohms at both ends. The termination prevents interfering signal reflections and ensures the proper operation of the transceivers of the connected CAN nodes (CAN interfaces, control devices).

The PCAN-miniPCIe FD card has an optional internal termination with 120 ohms. For more information, see chapter 3.3.

3.4.2 Example of a Connection

This example shows a connection between the PCAN-miniPCIe FD card and a control unit. The connection cable is terminated with 120 ohms at both ends.
3.4.3 Maximum Bus Length

High-speed CAN networks have bit rates up to 1 Mbit/s. The maximum bus length primarily depends on the bit rate.

The following table shows different maximum possible CAN bus length with different bit rates:

<table>
<thead>
<tr>
<th>Bit rate</th>
<th>Bus length</th>
</tr>
</thead>
<tbody>
<tr>
<td>1 Mbit/s</td>
<td>40 m</td>
</tr>
<tr>
<td>500 kbit/s</td>
<td>110 m</td>
</tr>
<tr>
<td>250 kbit/s</td>
<td>240 m</td>
</tr>
<tr>
<td>125 kbit/s</td>
<td>500 m</td>
</tr>
<tr>
<td>50 kbit/s</td>
<td>1.3 km</td>
</tr>
<tr>
<td>20 kbit/s</td>
<td>3.3 km</td>
</tr>
<tr>
<td>10 kbit/s</td>
<td>6.6 km</td>
</tr>
<tr>
<td>5 kbit/s</td>
<td>13.0 km</td>
</tr>
</tbody>
</table>

The listed values have been calculated on the basis of an idealized system and can differ from reality.

**Note:** For CAN FD, the same maximum bus lengths apply as for CAN, despite the higher data bit rate of CAN FD. The dependency is based on the bit rate during the arbitration, called nominal bit rate. The nominal bit rate at CAN FD can be up to 1 Mbit/s.
4 Software and API

This chapter covers the provided software PCAN-View and the programming interface PCAN-Basic.

4.1 Monitor Software PCAN-View

PCAN-View is simple Windows software for viewing, transmitting, and logging CAN and CAN FD messages.

Note: This chapter describes the use of PCAN-View with a CAN FD card.

Figure 12: PCAN-View for Windows
Do the following to start and initialize PCAN-View:

1. Open the Windows Start menu and select **PCAN-View**.
   The **Connect** dialog box appears.

   ![PCAN-View Connect dialog box](image)
   
   **Figure 13:** Selection of the hardware and parameters

2. Select an interface from the list.

3. From the drop-down menu, choose a **Clock Frequency**. The selectable bit rates in the following are based on this setting.

4. From the drop-down list, select a **Nominal Bit rate**, which is used for the arbitration phase (max. 1Mbit/s).

5. Enable the **Data Bit Rate** checkbox.

6. From the drop-down menu, choose an additional **Data Bit rate** for the CAN FD bus. The bit rate selected here is used to transfer the data fields of a CAN FD frame with a higher bit rate.

   **Note:** Both transmission rates must match those which are used by all nodes on the CAN bus.

   **Tip:** Create custom bit rates by using the arrow button (▶).
7. Under **Filter settings**, you can limit the range of CAN IDs to be received, either for standard frames (11-bit IDs) or for extended frames (29-bit IDs).

8. Activate the **Listen-only mode** if you do not actively participate in the CAN traffic and just want to observe. This also avoids an unintended disruption of an unknown CAN environment (e.g. due to different bit rates).

9. Confirm the settings in the dialog box with **OK**. The main window of PCAN-View appears (see Figure 14)
4.1.1 Receive/Transmit Tab

The Receive/Transmit tab is the main element of PCAN-View. It contains two lists, one for received messages and one for the transmit messages. The CAN data format is hexadecimal by default.

Do the following to transmit one CAN message with PCAN-View:

1. Select the menu command Transmit \(\rightarrow\) New Message (alternatively \(\text{Ctrl} + \text{N}\) or \(\text{Ins}\)).

The New Transmit Message dialog box appears.

Figure 14: Receive/Transmit tab

Figure 15: Dialog box New Transmit Message
2. Enable the **CAN FD** checkbox to define a CAN FD message with a maximum **Length** of 64 data bytes.

3. Enter the **ID**, the data **Length**, and the CAN message **Data**. With a length of more than 8 bytes, click on , and enter the data bytes into the editor.

**Note:** With the program version 4 of PCAN-View, the DLC field was renamed to **Length**. Latter reflects the actual data length.

4. Enter a value into the **Cycle Time** field to choose manually or periodically message transmission. Enter a value greater than 0 to transmit periodically. Enter the value 0 to transmit only manually.

5. Enable the **Bit Rate Switch** checkbox, that the data of a CAN FD message is transmitted with the selected **Data Bit Rate**.

6. Confirm the entries with **OK**.

   The created transmit message appears on the **Receive/Transmit** tab.

7. Trigger selected transmit messages manually with the menu command **Transmit > Send** (alternatively Space bar). The manual transmission for CAN messages being transmitted periodically is carried out additionally.

**Tip:** Under the menu command **File > Save**, you can save the current transmit messages into a transmit list. Saved transmit lists are available for reuse.
4.1.2 Trace Tab

On the Trace tab, the data tracer (data logger) of PCAN-View is used for logging the communication on a CAN bus. During this process the messages are cached in the working memory of the PC. Afterwards they can be saved to a file.

The Tracer runs either in linear or in ring buffer mode. The linear buffer mode stops the Tracer as soon as the buffer is full. The ring buffer mode overwrites the oldest messages by new ones as soon as the buffer is full.
4.1.3 PCAN-miniPCIe FD Tab

The PCAN-miniPCIe FD tab contains some detailed information about the hardware and driver.

**CAN FD ISO-mode**

The defined in the ISO 11898-standard is not compatible with the original protocol. PEAK-System takes this into account by supporting both protocol versions with their CAN FD interfaces.

If required, the user can switch to the CAN FD protocol used in the environment with the **Enable / Disable** button („Non-ISO“ and „ISO“).
4.1.4 Bus Load Tab

On the **Bus Load** tab, the current bus load, time course, and statistical information of the CAN channel are displayed. The CAN bus load reflects the utilization of transmission capacity.
4.1.5 Error Generator Tab

Via the **Error Generator** tab, the communication on the CAN bus can be disturbed for testing purposes by 6 consecutive dominant bits. This is a violation of the CAN protocol on the CAN bus which must be recognized as an error by the connected CAN nodes.

You can destroy CAN frames with the error generator by one of two methods:

- once after activation
- repeatedly at specific intervals related to a CAN ID

The **Destroy Single Frame** area refers to the next CAN frame that is recognized by the plug-in card after activation.

Do the following to destroy a single CAN frame:

1. Enter the **Bit Position** where in the CAN frame the error is to be generated. The count includes the stuff bits.
2. Execute the destroy action with **Do it**.

The next received or transmitted CAN frame will be destroyed at the selected bit position.

The **Destroy Multiple Frames** area refers to a CAN ID whose frames are to be destroyed in specific intervals.

![Do the following to destroy multiple CAN frames:](image)

1. Enter the **CAN ID** of the frame to be destroyed.

2. Enter the **Bit Position** where in the CAN frame the error is to be generated. The count includes the stuff bits.

3. The **Number of Frames to ignore** field specifies the number of CAN frames with the given ID that are ignored before a frame is destroyed.

4. The **Number of Frames to destroy** field specifies the number of CAN frames with the given ID that are destroyed consecutively.

5. Confirm the entries with **Apply** to activate the error generator.

6. Stop destroying further CAN frames with **Disable**.

### 4.1.6 Status Bar

![status bar](image)

The status bar shows information about the current CAN connection, about error counters (Overruns, QXmtFull) and shows error messages.

You can find further information about the use of PCAN-View in the help which you can invoke in the program via the **Help** menu or with the **F1** key.
4.2 Linking Own Programs with PCAN-Basic Version 4 or Higher

On the provided DVD, you can find files of the PCAN-Basic programming interface in the directory branch `Develop`. This API provides basic functions for linking own programs to CAN and CAN FD interfaces by PEAK-System and can be used for the following operating systems:

- Windows 10, 8.1, 7 (32/64-bit)
- Windows CE 6.x (x86/ARMv4)
- Linux (32/64-bit)
The API is designed for cross-platform use. Therefore, software projects can easily be ported between platforms with low efforts. For all common programming languages, examples are available.

Beginning with version 4, PCAN-Basic supports the new CAN FD standard (CAN with Flexible Data Rate) which is primarily characterized by higher bandwidth for data transfer.

4.2.1 Features of PCAN-Basic

- API for developing applications with CAN and CAN FD connections
- Support for CAN specifications 2.0 A/B and FD
- Supports the operating systems Windows 10, 8.1, 7 (32/64-bit), Windows CE 6.x, and Linux (32/64-bit)
- Multiple PEAK-System applications and your own can be operated on a physical channel at the same time
- Use of a single DLL for all supported hardware types
- Use of up to 16 channels for each hardware type (depending on the PEAK CAN interface used)
- Simple switching between channels of a PCAN PC hardware
- Access to the CAN channels of a PCAN-Gateway via the new PCAN-LAN hardware type
- Driver-internal buffering of up to 32,768 CAN messages per CAN channel
- Precision of time stamps on received messages up to 1 μs (depending on the PEAK CAN interface used)
- Supports PEAK-System’s trace formats version 1.1 and 2.0 (for CAN FD applications)
- Access to specific hardware parameters, such as listen-only mode
Notification of the application through Windows events when a message is received
Extended system for debugging operations
Multilingual debugging output
Output language depends on operating system
Debugging information can be defined individually
Thread-safe API

💡 Tip: An overview of the API functions is located in the header files. You can find detailed information about the PCAN-Basic API on the provided DVD in the text and help files (file name extensions .txt and .chm).

### 4.2.2 Principle Description of the API

The PCAN-Basic API is the interface between the user application and device driver. In Windows operating systems this is a DLL (Dynamic Link Library).

The sequence of accessing the CAN interface is divided into three phases:

1. Initialization
2. Interaction
3. Completion

**Initialization**

A channel must be initialized before using it. This is done by the simple call of the function `CAN_Initialize` for CAN and `CAN_InitializeFD` for CAN FD. Depending on the type of the CAN hardware, up to 16 CAN channels can be opened at the same time. After a successful initialization the CAN channel is ready. No further configuration steps are required.
Interaction

For receiving and transmitting messages, the functions `CAN_Read` and `CAN_Write` as well as `CAN_ReadFD` and `CAN_WriteFD` are available. Additional settings can be made, e.g. setting up message filters for specific CAN IDs or the listen-only mode for the CAN controller.

When receiving CAN messages, events are used for an automatic notification of an application (client). This offers the following advantages:

- The application no longer needs to check for received messages periodically (no polling).
- The response time at reception is reduced.

Completion

To end the communication, the function `CAN_Uninitialize` is called in order to release the reserved resources for the CAN channel, among others. In addition the CAN channel is marked as "Free" and is available to other applications.

4.2.3 Notes about the License

Device drivers, the interface DLL, and further files needed for linking are property of the PEAK-System Technik GmbH and may be used only in connection with a hardware component purchased from PEAK-System or one of its partners. If a CAN hardware component of third-party suppliers should be compatible to one of PEAK-System, then you are not allowed to use or to pass on the driver software of PEAK-System.

If a third-party supplier develops software based on the PCAN-Basic and problems occur during the use of this software, consult the software provider.
# 5 Technical Specifications

## Connectors

| Computer | PCI Express Mini slot, 52-pin; electromechanical; specifications 1.1 and 1.2 |
| CAN (via cable) | D-Sub (m), 9-pin, Pin assignment according to specification CiA® 303-1 |
| CAN (on card) | Connector type SUR from JST (www.jst-mfg.com), name of the matching plug: SM05B-SUR-TF |

## CAN

| Physical transmission | ISO 11898-2 (High-speed CAN) |
| CAN bit rates | 25 kbit/s - 1 Mbit/s |
| CAN FD bit rates | 25 kbit/s – 12 Mbit/s |
| Controller | FPGA implementation |
| Transceiver | NXP TJA1044GT |
| Galvanic isolation | Up to 300 V (separate for each CAN connector) |
| Daisy Chain | via solder bridges, not activated at delivery |
| Internal termination | via solder bridges, not activated at delivery |

## Power supply

| Supply voltage | 1.5 V and 3.3 V |
| Current consumption | **Model** | **on 1.5 V pin** | **on 3.3 V pin** |
| | Single Channel | max. 350 mA | max. 100 mA |
| | Dual Channel | max. 350 mA | max. 150 mA |
| | Four Channel | max. 350 mA | max. 250 mA |
### Measures

<table>
<thead>
<tr>
<th>Size</th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td>Single and Dual Channel:</td>
<td>30 x 51 x 4 mm (W x L x H)</td>
</tr>
<tr>
<td>Four Channel:</td>
<td>30 x 51 x 12 mm (W x L x H)</td>
</tr>
<tr>
<td>Four Channel:</td>
<td>30 x 51 x 12 mm (W x L x H)</td>
</tr>
</tbody>
</table>

See also dimension drawing in Appendix B on page 38.

<table>
<thead>
<tr>
<th>Weight</th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td>Single and Dual Channel card:</td>
<td>8 g</td>
</tr>
<tr>
<td>Four Channel card:</td>
<td>11 g</td>
</tr>
</tbody>
</table>

Cable for D-Sub connection:
- Single channel cable: 7 g
- Dual channel cable: 7 g

| Length connection cable (card - D-Sub) | 20 cm, other cable lengths on request |

### Environment

<table>
<thead>
<tr>
<th>Operating temperature</th>
<th>-40 - 85 °C (-40 - 185 °F)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Temperature for storage and transport</td>
<td>-40 - 100 °C (-40 - 212 °F)</td>
</tr>
<tr>
<td>Relative humidity</td>
<td>15 - 90 %, not condensing</td>
</tr>
</tbody>
</table>

### Conformity

<table>
<thead>
<tr>
<th>EMV</th>
<th>Directive 2014/30/EU</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>DIN EN 55024:2016-05</td>
</tr>
<tr>
<td></td>
<td>DIN EN 55032:2016-02</td>
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</tbody>
</table>

<table>
<thead>
<tr>
<th>RoHS 2</th>
<th>Directive 2011/65/EU</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>DIN EN 50581 VDE 0042-12:2013-02</td>
</tr>
</tbody>
</table>
Appendix A  CE Certificate

EU Declaration of Conformity

This declaration applies to the following product:
Product name: PCAN-miniPCiE FD
Item number(s): IPEH-004045/46/47
Manufacturer: PEAK-System Technik GmbH
Otto-Roehm-Strasse 69
64293 Darmstadt
Germany

We declare under our sole responsibility that the mentioned product is in conformity with the following directives and the affiliated harmonized standards:

EU Directive 2011/65/EU (RoHS 2)
DIN EN 50581 VDE 0042-12:2013-02
Technical documentation for the assessment of electrical and electronic products with respect to the restriction of hazardous substances;
German version EN 50581:2012

EU Directive 2014/30/EU (Electromagnetic Compatibility)
DIN EN 55024:2016-05
Information technology equipment – Immunity characteristics – Limits and methods of measurement (CISPR 24:2010 + Cor.:2011 + A1:2015);
German version EN 55024:2010 + A1:2015
DIN EN 55032:2016-02
Electromagnetic compatibility of multimedia equipment - Emission Requirements (CISPR 32:2015);
German version EN 55032:2015

Darmstadt, 22 February 2019

[Signature]

Uwe Wilhelm, Managing Director
Appendix B Dimension Drawing

Figure 22: View onto the PCAN-miniPCIe FD Single and Dual Channel (IPEH-004045/46)

Figure 23: View onto the PCAN-miniPCIe FD Four Channel (IPEH-004047). Due to its height (approx 12 mm), the four-channel version does not comply to the form factor PCI Express Mini.

The figures do not show the actual size of the products.
Appendix C Quick Reference

Software/Hardware Installation under Windows

Install the driver from the supplied DVD, before you insert the plug-in card. Turn off the computer and insert the PCAN-miniPCIe FD into an available PCI Express Mini slot.

The new hardware is detected at the next Windows start and the driver is initialized. Check the operational readiness. Open the Windows Start menu. Type `peakcpl` and press `Enter`. An information window for PEAK Hardware opens. The plug-in card must be displayed in the table on the **CAN Hardware** tab.

Getting Started under Windows

Run the CAN monitor PCAN-View from the Windows Start menu as a sample application for accessing the card. For initialization of the PCI Express Mini Card select the desired CAN channel and CAN bit rate.

High-speed CAN connector (D-Sub, 9 pins)